

06-05-01

OSTROLENK, FABER, GERB & SOFFEN, LLP  
Attorneys at Law  
1180 Avenue of the Americas  
New York, New York 10036-8403  
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(212) 382-0700



Express Mail # EL 583738104US

Assistant Commissioner for Patents  
Washington, DC 20231

OFGS File No. : P/2171-196  
Inventor : Harumitsu Fujita  
Title : MULTI-VOLTAGE LEVEL SEMICONDUCTOR DEVICE  
AND ITS MANUFACTURE  
Assignee : Yamaha Corporation

The present application is a divisional under 37 C.F.R. §1.53(b) of prior application Serial No. 09/021,519, filed February 10, 1998, by Harumitsu Fujita entitled MULTI-VOLTAGE LEVEL SEMICONDUCTOR DEVICE AND ITS MANUFACTURE.

Enclosed herewith please find the following documents in the above-identified application for United States Letters Patent:

- ☒ Print EFS Data Sheet
- 24 Pages of Specification including Abstract and Claims
- 8 Numbered Claims Calculated as 8 Claims for Fee Purposes
- 6 Sheets of Drawing Containing Figures 1A to 4L. (Formal)
- ☒ Copy of Declaration and Power of Attorney from the prior application under 37 CFR 1.63(d). The entire disclosure of the prior application is considered a part of the disclosure of the accompanying divisional application and is hereby incorporated herein by reference.
- \_\_\_\_ New Declaration and Power of Attorney
- ☒ Priority under 35 U.S.C. §119 based on Japanese Application No. 9-028132, filed February 12, 1997, is claimed. The certified copy of the priority application has been filed in said prior application Serial No. 09/021,519, filed February 10, 1998.
- ☒ The prior application was assigned to Yamaha Corporation and was recorded on February 10, 1998 at Reel 011516, Frame 0533
- ☒ Information Disclosure Statement and Form 1449
- ☒ Return-Addressed Post Card

OFGS Check No. 4919, which includes the fee of \$ 710, calculated as follows:

Basic Filing Fee: .....	\$ 710.00
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Total Number of Claims in Excess of 20, times \$18: .....	
Number of Independent Claims in Excess of 3, times \$80: .....	
One or More Multiple Dependent Claims: Total \$270. ....	
Total Filing Fees or .....	\$ <u>710.00</u>

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**REMARKS**

Please add the following paragraph after the title of the application:

-- Cross Referenced to Related Applications:

This is a divisional of U.S. Patent Application Serial No. 09/021,519, filed February 10, 1998, in the name of Harumitsu Fujita and entitled Multi-Voltage Level Semiconductor Device and Its Manufacture.--

**EXPRESS MAIL CERTIFICATE**

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail Post Office to Addressee (mail label # EL 583738104US) in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on :

Dorothy Jenkins

Name of Person Mailing Correspondence

*Dorothy Jenkins*

Signature

June 4, 2001

Date of Signature

Respectfully submitted,

*M. Pfeffer*

Martin Pfeffer

Registration No.: 20,808

OSTROLENK, FABER, GERB & SOFFEN, LLP

1180 Avenue of the Americas

New York, New York 10036-8403

Telephone: (212) 382-0700

09/021,519